

ABSTRACT

An object of the present invention is to provide a semiconductor device that can prevent performance degradation due to wasted execution cycles. According to the present invention, a fetch is performed in an information fetch block (IFB 2) on the basis of the status of a branch-taken signal (BRTKN 9) provided from an execution block (EXB 4), wherein if a conditional branch is taken, the IFB 2 selects a branch target address and if not, the IFB 2 selects an address of the next instruction. The IFB 2 then provides the selected address to an instruction memory (1) to control the fetch in accordance with the address.